

## Change to EBR Modes Supported in the LatticeECP2/M Family

The Read-Before-Write sysMEM<sup>™</sup> Embedded Block RAM (EBR) mode is no longer a supported feature in the LatticeECP2<sup>™</sup> and LatticeECP2M<sup>™</sup> families. The other two EBR modes, Normal and Write Through, are still fully supported. Please contact Lattice Technical Support (techsupport@latticesemi.com) for potential alternate implementations of applications that use the Read-Before-Write mode.

All devices in the LatticeECP2/M family (Ordering Part Number prefixes LFE2 and LFE2M) are affected by this change, including custom specifications. All shipments of LatticeECP2/M product to customers prior to this product bulletin are also affected.

The revised LatticeECP2/M data sheet, DS1006, Version 03.2 released in June 2008 reflects this change. Technical Note TN1104, LatticeECP2/M Memory Usage Guide, has also been updated to reflect this change. The data sheet and technical note are available on the Lattice website: <http://www.latticesemi.com>.

Additionally, the Read-Before-Write function will not be supported in ispLEVER<sup>®</sup> Version 7.1 Service Pack 1, available in August 2008. Please upgrade to ispLEVER Version 7.1 Service Pack 1 as soon as it is available on the Lattice website: <http://www.latticesemi.com/software>.

We apologize for this inconvenience. If you require a formal Product Change Notification (PCN) regarding this change, have any questions or require additional information, please contact your local Lattice sales representative.