



## Product Change Notice

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<b>Date of Publication:</b>	May 21, 2010
<b>PCN No:</b>	PCN-10-047
<b>Change Subject:</b>	Product Migration from MX25L2025 / MX25L2025C to MX25L2026E
<b>Change Type:</b>	Product Migration
<b>Key Milestones:</b>	Last Order Date: Feb. 28, 2011 Last Shipping Date: May. 31, 2011

### Description of Changes

Macronix introduces the MX25L2026E as a replacement of the MX25L2025 / MX25L2025C. All customers are requested to start the migration for their existing design to the MX25L2026E as soon as possible.

MX25L2026E supports new features, SFDP mode and Dual Output mode (Single Input / Dual Output).

For detail of these changes, please refer to Application Notes of MX25L2026E.

### Impact of Changes and Recommended Actions

Besides original function of MX25L2025 / MX25L2025C, the MX25L2026E provides new features that make no impact on customer's original design.

Applications using the MX25L2025 / MX25L2025C need to migrate to the MX25L2026E, and new designs should only use the MX25L2026E. Migration to MX25L2026E will enable Macronix to provide continuous support to our customers.

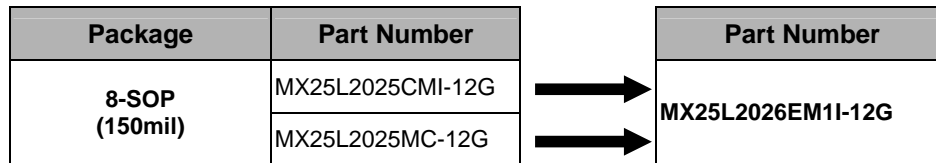
## General Feature Comparison:

Specification		MX25L2025 / MX25L2025C	MX25L2026E
Voltage		2.7~3.6 V	2.7~3.6 V
Interface		x1	x1, 1I /2O*
Package		8-SOP (150mil)	8-SOP (150mil )
Operating Temperature		-40~85 °C	-40~85 °C
Sector / Block		4KB /64KB	4KB / 64 KB
Clock Rate	Fast Read	1KHz~85MHz	DC~86MHz
	Read	1KHz~33MHz	DC~33MHz
	DREAD (Dual Output)	NA	DC~80MHz
Byte Program		NA	Yes
SFDP Mode		NA	Yes
Block Protection (SRAM Protect)		BP1, BP0	BP1, BP0
Hold#		Yes	Yes
WP#		Yes	Yes

**Note:**

\* Dual Output mode (1I /2O) means Single Input / Dual Output.

## Products Affected:



## Production Status for E version:

▶ Sample Availability	May/2010
▶ Mass Production	June/2010

For the contact and order information, please visit Macronix's Web site at:

<http://www.macronix.com>