

Macronix
Process Change Notification (PCN)

MX29LV160D
(3V, 16Mb, Parallel Flash)

Date Issued : May, 2008

Issued by : PM/ Memory SBU

S/N:F-PCN-24

MXIC Proprietary & Confidential



MACRONIX
INTERNATIONAL CO., LTD.

Dear Macronix Customer,

Macronix is introducing the 5th generation of 16Mb parallel flash product. Macronix has shrunk the die size from 0.15um to 0.13um in order to continue to offer our customers the most advanced and cost effective products.

In order to achieve better performance, reliability, and cost structure, Macronix is introducing the “D” revision of 16Mb parallel flash products on our advanced 0.13um technology.

Product is available in samples and mass production now.

The MX29LV160D device is identical in form, fit and function to the previous generation MX29LV160C product. Additional functions added in this version are – hardware write protection and accelerated program capability.

Macronix requests that you kindly qualify the new version as quickly as possible in order for us to continue supporting you without interruption.

Thank you

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Feature Comparison Table

Part No.	MX29LV160C	MX29LV160D	Notes
Process	0.15um FG	0.13um FG	
VCC range	2.7V ~ 3.6V	2.7V ~ 3.6V	
Read Speed	55R/70/90 ns	70ns	
CFI Query	Yes	Yes	Please note the CFI data between LV160C and LV160D is different
Icc Standby	5uA (max.)	15uA (max.)	
Active Read Current	12mA (max, @5MHz)	12mA (max, @5MHz)	
Active Write Current (Erase/Program)	30mA (max.)	30mA (max.)	
Chip Erase Time	15sec (typ.) / 32sec (max.)	15sec (typ.) / 32sec (max.)	
Sector Erase Time	0.7sec (typ.) / 15sec (max.)	0.7sec (typ.) / 2sec (max.)	
Chip Programming time (word mode)	12sec (typ.) / 36sec (max.)	12sec (typ.) / 36sec (max.)	
Word Program Time	11us (typ.) / 360us (max.)	11us (typ.) / 360us (max.)	
Package Type	44-SOP 48-TSOP 48-TFBGA (6x8mm) 48-LFBGA (6x8mm)	48-TSOP 48-TFBGA (6x8mm) 48-WFBGA (4x6mm) 48-XFLGA (4x6mm)	Please note that LV160D add WP#/ACC pin

Additional Functions	MX29LV160C	MX29LV160D	Notes
Hardware Write Protection	Not Available	Available	
Accelerated Programming	Not Available	Available	

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Part Numbering Change

MX29LV160C

48-TSOP

MX29LV160C(T/B)TC-55Q/70G/90G

MX29LV160C(T/B)TI-55Q/70G/90G

48-TFBGA (6x8mm)

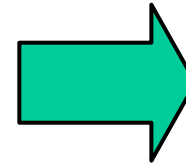
MX29LV160C(T/B)XBC-55Q/70G/90G

MX29LV160C(T/B)XBI-55Q/70G/90G

48-LFBGA (6x8mm)

MX29LV160C(T/B)XEC-55Q/70G/90G

MX29LV160C(T/B)XEI-55Q/70G/90G



MX29LV160D

48-TSOP

MX29LV160D(T/B)TI-70G

48-TFBGA (6x8mm)

MX29LV160D(T/B)XBI-70G

48-TFBGA (6x8mm)

MX29LV160D(T/B)XBI-70G

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Conversion Schedule

➤ “D” version samples available:	Now
➤ “D” version mass production :	6 th of Mar. 2008
➤ Old version last order placement date:	31 st of Dec. 2008
➤ Old version final shipment date:	31 st of Mar. 2009

Macronix Recommendations:

- Macronix requests that you qualify the new D version samples now. This will allow Macronix to continue supporting you without interruption
- Datasheets are available on our website (www.macronix.com)